

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Sung Il KIM, et al.

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: March 19, 2004 Examiner: Not Yet Assigned

Title: OPTICAL TRANSCEIVER FOR REDUCING CROSSTALK

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 19, 2004

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

JACOBSON HOLMAN PLLC

By: 

Yoon S. Ham

Registration No. 45,307

400 Seventh Street, N.W.
Washington, DC 20004
(202) 638-6666
Atty. Dkt. No.: P69590US0
YSH:dj

JACOBSON HOLMAN PLLC
 400 SEVENTH STREET, N.W.
 WASHINGTON, D.C. 20004-2201

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P69590US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: New Application FILING DATE: March 19, 2004
 APPLICANT(S): Sung Il KIM, et al. TODAY'S DATE: March 19, 2004

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
	AA 6,552,366	04/22/03	Terada et al.	H01L	33/00	07/24/98
	AB					
	AC					

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AD					
AE					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AF Hideaki Kimura, et al.; "A Low-Crosstalk Optical Module Design on PLC Platform for Realizing LD/PD Full-Duplex Operation in ATM Systems"; ECOC '98; September 20-24, 1998; pp. 481-482.

AG _____

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).